

# SPECIFICATIONS

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Seven segment Display **数码管产品规格书**

# TOPLITE



**MODEL: TOP-CA-3121BH**

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SHANGHAI TOPLITE TECHNOLOGY CO.,LTD.

[www.ledtoplight.com.cn](http://www.ledtoplight.com.cn)  
[www.ledtoplite.com](http://www.ledtoplite.com)



## TECHNICAL DATA SHEET

**TOP-CA-3121BH** FOR 0.31 inch (8.0mm) digit height.

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### 1. PRODUCT INTRODUCTION

- ※ Low power requirement,
- ※ Solid state reliability.
- ※ Wide viewing angle.
- ※ Easy mounting on P.C. boards.
- ※ RHOS compliant.

### 2. FEATURES

- ※ **TOP-CA-3121BH** is a 1.0 INCH (25.4mm) SINGLE DIGIT.
- ※ This device is made with white dots and black surface.

### 3. APPLICATION

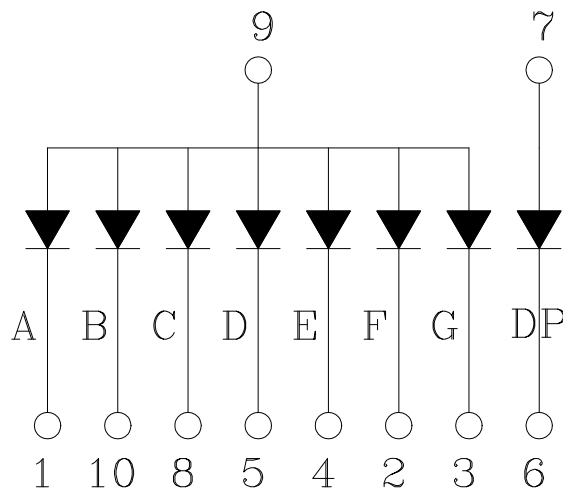
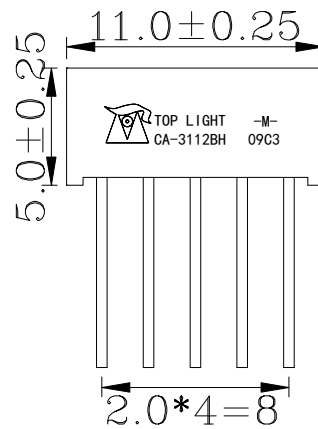
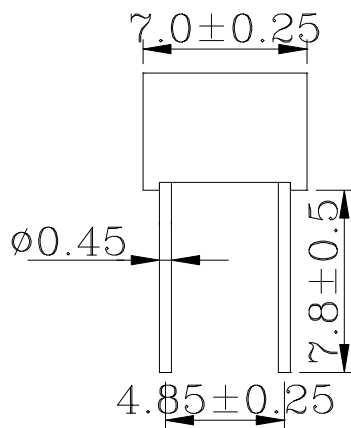
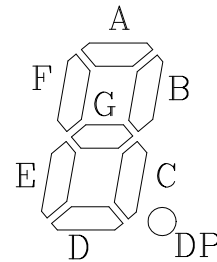
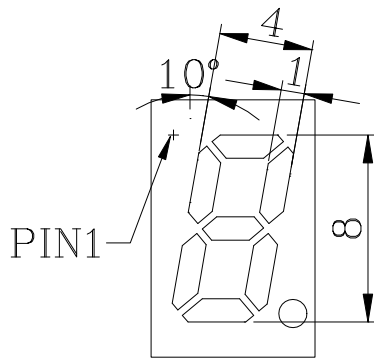
- ※ Digital readout display.
- ※ Instrument panels.
- ※ Elevator.

PART NO.	SIZE	CHIP EMITTED COLOR	FACE COLOR
<b>TOP-CA-3121BH</b>	0.31 inch (8.0mm)	Red	Black

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## 4. PACKAGE DIMENSIONS & CIRCUIT DIAGRAM



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**5. ELECTRICAL/OPTICAL CHARACTERISTIC****5-1. ABSOLUTE MAXIMUM RATINGS (Ta=25°C)**

PARAMETER	SYMBOL	VALUE	UNIT
Reverse Voltage	$V_R$	5	V/seg
Forward Current	$I_F$	30	mA/seg
Peak Forward Current (1/10 Duty Cycle)	$I_{PEAK}$	120	mA/seg
Power Dissipation	$P_D$	80	mW/seg
Operating Temperature Range	$T_A$	- 25 ~ + 85	°C
Storage Temperature Range	$T_{STG}$	- 30 ~ + 85	°C
Solder Temperature	$T_{sol}$	260/3	°C/s

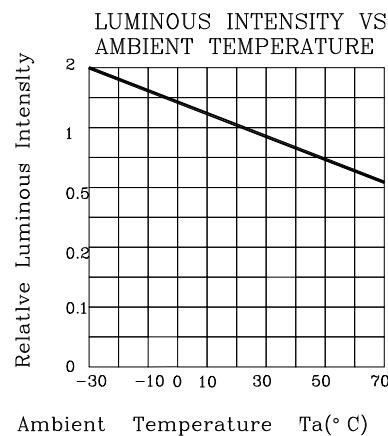
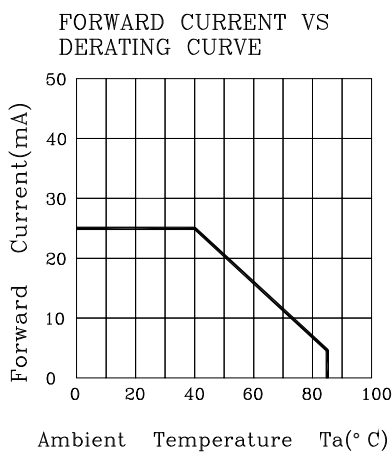
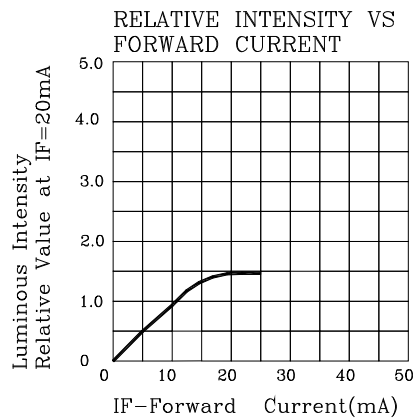
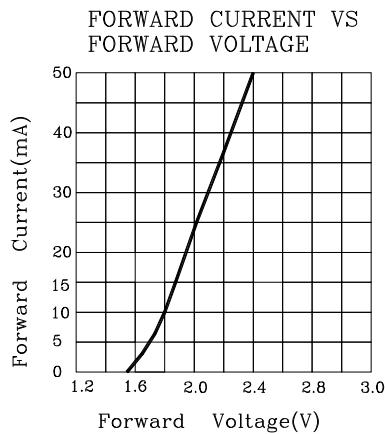
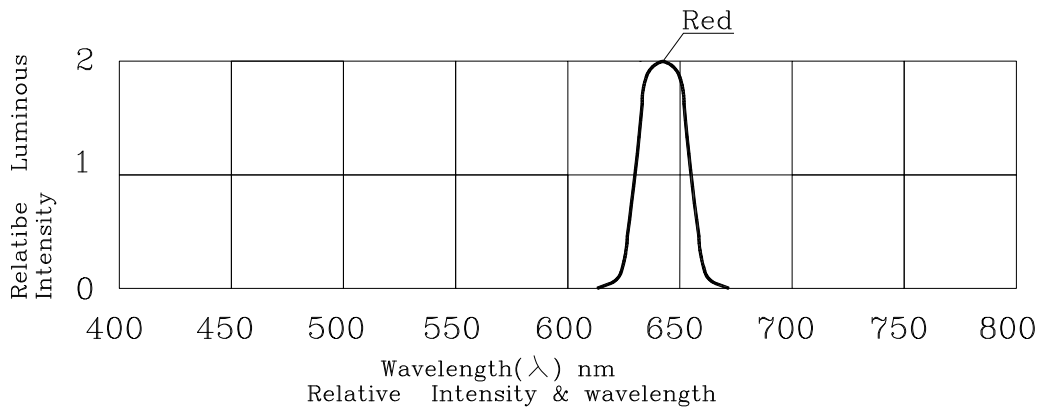
**5-2. ELECTRICAL-OPTICAL CHARACTERISTICS (Ta=25°C)**

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS	
Luminous Intensity	$I_V$	K	4726	5435	6144	ucd	$I_F=10mA$
		M	6145	7066	7987		
		N	7988	9185.5	10383		
Forward Voltage	$V_F$	1.70	1.85	2.20	v/seg	$I_F=20mA$	
Chromatographer	$\lambda_p$	-	640	-	nm	$I_F=20mA$	
Spectral Line Half-Width	$\Delta \lambda$	-	20	-	nm	$I_F=20mA$	
Reverse Current	$I_R$	-	-	20	uA	$V_R=5v$	

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**5-3. OPTICAL CHARACTERISTIC CURVES**





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### 6. QUALITY CONTROL AND ASSURANCE

CLASSIFICATION	TEST ITEM	TEST CONDITION
ENDUTRANCE TEST	OPERATION LIFE	Ta=under room temperature If=12mA-25mA per segment or Ip=80mA/duty=1/8,Pw=1.25mS Ip=160mA/duty=1/16,Pw=1.mS(DOT) Test time=1000HRS(-24HRS+72HRS)
	MOISTURE	Ta=65°C±5°C RH=90-95% Test time=240HRS±2HRS
	HIGH TEMPERATURE HIGH HUMIDITY REVERSE BIAS	Ta=65°C±5°C RH=90-95% VR=5V Test time=500hrs(-24HRS+48HRS)
	HIGH TEMPERATURE STORAGE	To evaluate device's durability for long term storage in high temperature Ta=85°C±5°C Test time=1000HRS(-24HRS+72HRS)
	LOW TEMPERATURE STORAGE	Ta=-35°C±5°C Test time=1000HRS(-24HRS+72HRS)
ENVIRONMENTAL TEST	TEMPERATURE CYCLING	Ta=85°C~25°C~-35°C time=30min 5min 30min 5min Cycle test:10cycles
	THERMAL SHOCK	Ta=85°C±5°C~-35°C±5°C time=10min 10min Cycle test:10cycles
	SOLOER RESISTANCE	T.sol=260°C±5°C time=10±1sec
	SOLOER ABILITY	T.sol=230°C±5°C time=5±1sec

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### 7. SOLDERING CONDITIONS

The recommended conditions for soldering are as follows.

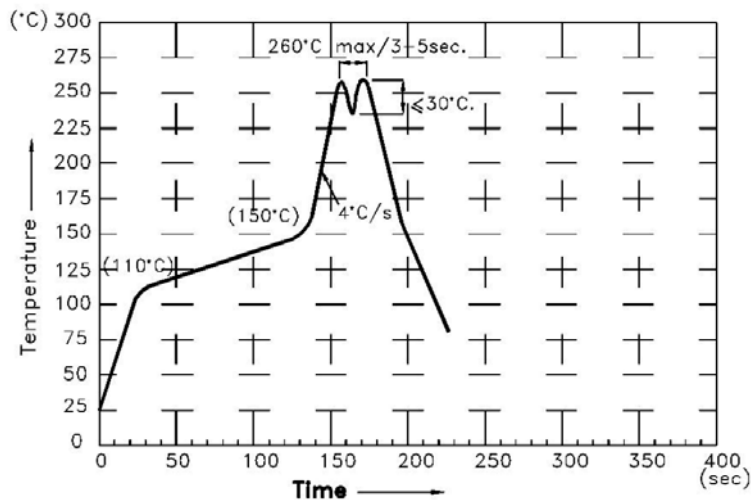
Because the component is made with epoxy resin, the units are susceptible to heat. Therefore, the preheating and soldering temperatures should be kept as low as possible to avoid damage.

#### 7-1. Manual Soldering Conditions(with 1.5mm Iron tip ).

Iron Tip Temperature: 350°C Max, Time: 3s Max.

Position: The iron should be situated at least 2mm away from the root of the leads.

#### 7-2. Through the Wave Soldering Conditions Wave Soldering Profile For Lead-free Through-hole LED.



#### 7-3. Soldering General Notes:

- Recommend manual soldering to be used only for repair and rework purposes. The soldering iron should not exceed 30W in power. The tip of the soldering iron should not touch the reflector case to avoid heat-damage.
- Maintain the pre-heat and peak temperatures with dip units as low as possible and the times as short as is feasible, since the products are susceptible to heat during flow soldering.
- After soldering, least three minutes for the component to cool to room temperature before further operations.
- If components will undergo multiple soldering processes, or other processes where the components may be subjected to intense heat, please check with toplight for compatibility.